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Dkt. 2271/70977

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Hitoshi YAMAMOTO

Serial No.: 10/656,434

Group Art Unit: 2826

Date Filed: September 5, 2003

Examiner: Alexander O. Williams

For: SEMICONDUCTOR DEVICE RESPONSIVE TO DIFFERENT LEVELS OF INPUT AND OUTPUT SIGNALS AND SIGNAL PROCESSING SYSTEM USING THE SAME (as amended)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

PETITION FOR EXTENSION OF TIME

It is respectfully requested that the period for filing a response to the Office Action dated September 16, 2004 be extended by one month, i.e. from December 16, 2004 to January 16, 2005.

The Commissioner is authorized to charge the \$120.00 statutory extension fee for response within the first month pursuant to 37 C.F.R. §1.136 (a) by other than a small entity, and any additional fees, or credit any overpayment, during prosecution of this application to our Deposit Account No. 03-3125.

Respectfully submitted,

Paul Teng

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I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Paul Teng
Paul Teng
Reg. No. 40,837

January 17, 2005
Date